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- 1/9/1
 DIALOG(R) File 345: Inpadoc/Fam. & Legal Stat
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 8954959
 Basic Patent (No, Kind, Date): JP 1265348 A2 19891023 < No. of Patents: 003>
 PATENT FAMILY:
 JAPAN (JP)
   Patent (No, Kind, Date): JP 1265348 A2 19891023
     GRAPHIC PROCESSOR (English)
     Patent Assignee: HITACHI LTD
     Author (Inventor): KATSURA AKIHIRO; KOJIMA SHINICHI; KURAGAMI NORIYUKI
     Priority (No, Kind, Date): JP 8893448 A 19880418
     Applic (No, Kind, Date): JP 8893448 A 19880418
     IPC: * G06F-013/36; G06F-012/00; G06F-015/64; G06F-015/66
     JAPIO Reference No: ; 140025P000115
     Language of Document: Japanese
   Patent (No, Kind, Date): JP 95036162 B4 19950419
     Priority (No, Kind, Date): JP 8893448 A 19880418
     Applic (No, Kind, Date): JP 8893448 A 19880418
     IPC: * G06F-012/00; G06F-013/36; G06T-001/00; G06T-001/60
     Derwent WPI Acc No: * G 90-382850
     JAPIO Reference No: * 140025P000115
     Language of Document: Japanese
 UNITED STATES OF AMERICA (US)
   Patent (No, Kind, Date): US 4975857 A 19901204
     GRAPHIC PROCESSING APPARATUS UTILIZING IMPROVED DATA TRANSFER TO REDUCE
       MEMORY SIZE (English)
     Patent Assignee: HITACHI LTD (JP)
     Author (Inventor): KATSURA KOYO (JP); KOJIMA SHINICHI (JP); KURAKAMI
       NORIYUKI
                 (JP)
     Priority (No, Kind, Date): JP 8893448 A
                                               19880418
     Applic (No, Kind, Date): US 302332 A
                                           19890127
     National Class: * 364518000; 340798000; 364521000
     IPC: * G06F-015/20
     Derwent WPI Acc No: ; G 90-382850
     Language of Document: English
 UNITED STATES OF AMERICA (US)
   Legal Status (No, Type, Date, Code, Text):
     US 4975857
                         19880418 US AA
                     Р
                                               PRIORITY (PATENT)
                               JP 8893448 A
                                             19880418
     US 4975857
                         19890127 US AE
                                               APPLICATION DATA (PATENT)
                               (APPL. DATA (PATENT))
                               US 302332 A 19890127
                         19890405 US AS02
     US 4975857
                     Ρ
                                               ASSIGNMENT OF ASSIGNOR'S
                               INTEREST
                               HITACHI, LTD., 6, KANDA SURUGADAI 4-CHOME,
                               CHIYODA-KU, TOKYO, JAPAN, A CORP. OF;
                               KATSURA, KOYO : 19890222; KOJIMA, SHINICHI :
                               19890222; KURAKAMI, NORIYUKI : 19890222
     US 4975857
                     Р
                         19901204 US A
                                                PATENT
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                         19930713 US RF
                                                REISSUE APPLICATION FILED
                               (REISSUE APPL. FILED)
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921203 20000627 US RF

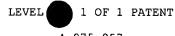
20000328

(REISSUE APPL. FILED)

REISSUE APPLICATION FILED

US 4975857

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Dec. 4, 1990

LEXIS-NEXIS Library: PATENT

File: **ALL**

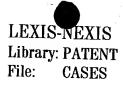
Graphic processing apparatus utilizing improved data transfer to reduce memory size

REISSUE: Reissue Application filed Mar. 28, 2000 (O.G. Jun. 27, 2000) Ex. Gp.: 2763; Re. S.N. 09/536,646

Reissue Application filed Dec. 3, 1992 (O.G. Jul. 13, 1993) Ex. Gp.: 2301

CORE TERMS: memory, cycle, display, bit, graphic, processing, column, row, buffer, pixel...

4,975,857 OR 4975857



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For further explanation, press the H key (for HELP) and then the ENTER key.

4,975,857 OR 4975857



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To enter a new search request, type it and press the ENTER key.

What you enter will be Search Level 1.

For further explanation, press the H $\ensuremath{\mbox{key}}$ (for HELP) and then the ENTER $\ensuremath{\mbox{key}}.$

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PN - US 4975857 [US4975857]
    - US 302332/89 19890127 [1989US-0302332]
AΡ
DT
   - US-P
ACT - 19890127 US/AE-A
      APPLICATION DATA (PATENT)
      {US 302332/89 19890127 [1989US-0302332]}
    - 19890405 US/AS02
      ASSIGNMENT OF ASSIGNOR'S INTEREST
      HITACHI, LTD., 6, KANDA SURUGADAI 4-CHOME, CHIYODA-KU, TOKYO, JAPAN, A
      CORP. OF * KATSURA, KOYO : 19890222; KOJIMA, SHINICHI : 19890222;
      KURAKAMI, NORIYUKI : 19890222
    - 19901204 US/A
      PATENT
    - 19930713 US/RF
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    - 20000627 US/RF
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UP - 2000-27
2/4 CRXX (1/1) - (C) CLAIMS/RRX
   - 2100971
   - 4,975,857 A 19901204 [US4975857]
   - Hitachi Ltd JP
   - E (Electrical)
ACT - 19921203 REISSUE REQUESTED
      Issue Date of O.G.: 19930713
      Reissue Request Number: 07/985141
      Examination Group responsible for Reissue process: 2301
    - 20000328 REISSUE REQUESTED
      Issue Date of O.G.: 20000627
      Reissue Request Number: 09/536646
      Examination Group responsible for Reissue process: 2763
UP - 1999-00
UACT- 2000-06-27
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AN - 200026-001307
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   - A (UTILITY)
DΤ
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SH - REISSUE APPLICATION FILED
4/4 PAST (2/2) - (C) PAST
AN - 199327-000963
PN - 4975857 A [US4975857]
DT - A (UTILITY)
OG - 1993-07-13
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